AC/DC Converter

PWM Type DC/DC Converter IC
Built-in a Switching MOSFET

BM2P0161-Z  BM2P0361-Z

General Description
The PWM type DC/DC converter for AC/DC provides an optimal system for all products that require an electrical outlet.
BM2P0161-Z and BM2P0361-Z support both isolated and non-isolated devices, enabling simpler design of various types of low power consumption electrical converters.
The built-in 650 V starter circuit contributes to low-power consumption.
Power supply can be designed flexibly by connecting current sensing resistor for the switching externally. Current is restricted in each cycle and excellent performance is demonstrated in bandwidth and transient response since current mode control is utilized. The switching frequency is 65 kHz. At light load, the switching frequency is reduced and high efficiency is achieved. A frequency hopping function that contributes to low EMI is also included on chip.
Design can be easily implemented because includes a 650 V switching MOSFET.

Feature
- PWM Frequency: 65 kHz
- PWM Current Mode Control
- Built-in Frequency Hopping Function
- Burst Operation When Load is Light
- Frequency Reduction Function
- Built-in 650 V Starter Circuit
- Built-in 650 V Switching MOSFET
- VCC Pin Under-Voltage Protection
- VCC Pin Over-Voltage Protection
- SOURCE Pin Open Protection
- SOURCE Pin Short Protection
- SOURCE Pin Leading Edge Blanking Function
- Per-Cycle Over-Current Protection Circuit
- Over Current Protection AC Voltage Compensation Circuit
- Soft Start
- Secondary Over-Current Protection Circuit

Key Specification
- Operating Power Supply Voltage Range:
  VCC: 8.9 V to 26.0 V
  DRAIN: 650 V(Max)
- Circuit Current (ON)1:
  BM2P0161-Z: 0.90 mA(Typ)
  BM2P0361-Z: 0.65 mA(Typ)
- Circuit Current (ON)2: 0.30 mA(Typ)
- Oscillation Frequency1: 65 kHz(Typ)
- Operating Ambient Temperature: -40 °C to +105 °C
- MOSFET ON Resistance:
  BM2P0161-Z: 1.0 Ω(Typ)
  BM2P0361-Z: 3.0 Ω(Typ)

Package
- DIP7K
  W (Typ) x D (Typ) x H (Max)
  9.27 mm x 6.35 mm x 8.63 mm
  pitch 2.54 mm

Application
For AC Adapters, TV and Household Appliances (Vacuum Cleaners, Humidifiers, Air Cleaners, Air Conditioners, IH Cooking Heaters, Rice Cookers, etc.)

Typical Application Circuit

○Product structure: Silicon monolithic integrated circuit  ○This product has no designed protection against radioactive rays.
Pin Configuration

Pin Description

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Pin Name</th>
<th>I/O</th>
<th>Function</th>
<th>ESD Diode</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>VCC</td>
</tr>
<tr>
<td>1</td>
<td>SOURCE</td>
<td>I/O</td>
<td>MOSFET SOURCE pin</td>
<td>✔</td>
</tr>
<tr>
<td>2</td>
<td>FADJ</td>
<td>I</td>
<td>Burst frequency setting pin</td>
<td>✔</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>I/O</td>
<td>GND pin</td>
<td>✔</td>
</tr>
<tr>
<td>4</td>
<td>FB</td>
<td>I</td>
<td>Feedback signal input pin</td>
<td>✔</td>
</tr>
<tr>
<td>5</td>
<td>VCC</td>
<td>I</td>
<td>Power supply input pin</td>
<td>-</td>
</tr>
<tr>
<td>6</td>
<td>DRAIN</td>
<td>I/O</td>
<td>MOSFET DRAIN pin</td>
<td>-</td>
</tr>
<tr>
<td>7</td>
<td>DRAIN</td>
<td>I/O</td>
<td>MOSFET DRAIN pin</td>
<td>-</td>
</tr>
</tbody>
</table>
Block Diagram

AC → FUSE → Filter → Diode Bridge → VH

VCC → S → GND

DRAIN → Feedback

VCC UVLO → 13.5V/8.2V → 27.5V → VCC DVP

10µs Filter

PWM Control + Burst Control

Burst Frequency Control

Current Limiter

Leading Edge Blanking (Typ=250ns)

Soft Start

AC Voltage compensation

Frequency Hopping

Slope Compensation

MAX DUTY

OSC (65kHz)

128ms/510ms Timer

Burst Comparator

PWM Comparator

4.0V Line Reg

12V Clamp Circuit

Starter

10µA

4.0V

1M

SOURCE

Rs

GND

Feedback

With Isolation

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TSZ22111 • 15 • 001
3/19
TSZ02201-0F1F0A200310-1-2
Description of Blocks

1. Start Circuit (DRAIN: Pin 6,7)
These ICs have a built-in start circuit. It enables low standby mode electricity and high speed start. After start up, consumption power is determined by idling current $I_{\text{START3}}$ only. Reference values of starting time are shown in Figure 3. When $C_{\text{VCC}}=10 \mu\text{F}$ it can start in less than 0.1 s.

* Start current flows from the DRAIN pin.

\[ PVH = 100V \times \sqrt{2} \times 10\mu A = 1.41\text{mW} \]

\[ PVH = 240V \times \sqrt{2} \times 10\mu A = 3.39\text{mW} \]
Description of Blocks - continued

2. Start Sequences

Start sequences are shown in Figure 4. See the sections below for detailed descriptions.

A: Input voltage VH is applied.
B: This IC starts operating when VCC>V_{UVLO1}. Switching function starts when other protection functions are judged as normal. Until the secondary output voltage becomes constant value or more from startup, the VCC pin consumption current causes the VCC voltage to drop. As a result, IC should be set to VCC>V_{UVLO2} until switching starts.
C: With the soft start function, over current limit value is restricted to prevent any excessive rise in voltage or current.
D: When the switching operation starts, VOUT rises.
   After a switching operating start, set the rated voltage within the t_{FOLP1} period.
E: When there is a light load, it makes FB voltage<V_{BST}. Burst operation is used to keep power consumption down.
F: When the FB pin voltage> V_{FOLP1A}, it overloads.
G: When the FB pin voltage> V_{FOLP1A} keeps above t_{FOLP1}, overcurrent protection is caused between t_{FOLP2} period, and switching stops. If the FB pin voltage< V_{FOLP1B}, the ICs internal timer t_{FOLP1} is reset.
H: If the VCC voltage< V_{CHG2}, recharge operation raises the VCC voltage.
I: Same as F.
J: Same as G.

Figure 4. Start Sequences Timing Chart
Description of Blocks – continued

3. VCC Pin Protection Function

These ICs have a built-in VCC UVLO (Under Voltage Lockout), VCC OVP (Over Voltage Protection), and a VCC recharge function that operates in case of a drop in VCC voltage.

VCC charge function stabilizes the secondary output voltage, charged from high voltage lines by the start circuit when VCC voltage drops.

(1) VCC UVLO / VCC OVP Function

VCC UVLO and VCC OVP are the self-recovery type comparator having voltage hysteresis.

![VCC UVLO / OVP Timing Chart](image)

- **A**: DRAIN voltage inputs, the VCC pin voltage starts rising.
- **B**: VCC>V_{UVLO1}, VCC UVLO function is released and DC/DC operation starts.
- **C**: VCC>V_{CHG1}, VCC charge function operates and the VCC voltage rises.
- **D**: VCC>V_{CHG2}, VCC charge function stops.
- **E**: VCC>V_{OVP1}, LATCH (100 μs Typ) continues, switching is stopped by the VCC OVP function.
- **F**: VCC<V_{OVP2}, DC/DC operation restarts.
- **G**: VH is OPEN, VCC Voltage falls.
- **H**: Same as C.
- **I**: Same as D.
- **J**: VCC<V_{UVLO2}, VCC UVLO function is detected and DC/DC operation stops.

Figure 5. VCC UVLO / OVP Timing Chart
3. VCC Pin Protection Function – continued

(2) VCC Charge Function
These ICs have the recharge function.
IC starts when the VCC pin voltage $>V_{UVLO1}$. When VCC voltage becomes $V_{CHG1}$ after IC started, VCC recharge function works. At that time the VCC pin is charged from the DRAIN pin through the start circuit.
Through this operation, these series prevent failure of VCC startup.
When the VCC pin voltage rises until $V_{CHG2}$, it finishes recharge. The operation is shown in Figure 6.

![Figure 6. VCC Pin Charge Operation](image)

A: The DRAIN pin voltage rises, charges the VCC pin through the VCC charge function.
B: $V_{CHG1}$, VCC UVLO function releases, VCC charge function stops, DC/DC operation starts.
C: Because output voltage is low, the VCC voltage drops at the start time.
D: $V_{CHG1}$, VCC recharge function operates, and the VCC pin voltage rise.
E: $V_{CHG2}$, VCC recharge function stops.
F: $V_{CHG2}$, VCC recharge function operates, and the VCC pin voltage rise.
G: $V_{CHG2}$, VCC recharge function stops.
H: After the output voltage is finished rising, VCC is charged by the auxiliary winding, and the VCC pin stabilizes.
4. DC/DC Driver

These ICs have a current mode PWM control. An internal oscillator sets a fixed switching frequency (65 kHz Typ). It has a switching frequency hopping function, which causes the switching frequency to fluctuate as shown in Figure 7 below. The fluctuation cycle is 125 Hz (Typ).

![Frequency Hopping Function](image)

Figure 7. Frequency Hopping Function

Maximum duty cycle is fixed at 75% and minimum ON time is fixed at 400 ns. In current mode control, sub-harmonic oscillation may occur when the duty cycle exceeds 50%. As a countermeasure, this IC has built-in slope compensation circuits. These ICs have built-in burst mode and frequency reduction circuits to achieve lower power consumption when the load is light.

The FB pin is pulled up to an internal power supply by \( R_{FB} \).

The FB pin voltage is changed by secondary output voltage (secondary load power). Monitor the FB pin voltage and change a switching operation state.

Figure 8 shows the FB voltage, and the DC/DC switching frequency operation.

- **mode1**: Burst operation.
- **mode2**: Frequency reduction operation. (max frequency is reduced)
- **mode3**: Fixed frequency operation. (operates at max frequency)
- **mode4**: Overload operation. (stops the pulse operation, sampling operation)

![Switching Operation State Changes by FB Pin Voltage](image)

Figure 8. Switching Operation State Changes by FB Pin Voltage
4. DC/DC Driver – continued

(1) Burst Frequency Setting

The frequency can be fixed by adding capacitance to the FADJ pin. This can reduce the burst sounds. The correlation between the capacitor connected to the FADJ pin and frequency is shown in the Figure 10.

Figure 9-1. No setting

Figure 9-2. setting

Figure 10. $f_{BST}$ vs $C_{FADJ}$
Description of Blocks – continued

5. **Over Current Limiter**

These ICs have a built-in over current limiter per switching cycle. If the SOURCE pin exceeds a certain voltage, switching stops. It also has a built-in AC voltage compensation function. This function is a compensation function to increase the over current limiter level by AC voltage compensation function time. Shown in Figure11,12,13.

Primary peak current is calculated using the formula below.

\[
I_{\text{peak}} = \frac{V_{\text{SOURCE}}}{R_s} + \frac{V_{\text{dc}}}{L_p} \times t_{\text{delay}}
\]

Where:
- \( V_{\text{SOURCE}} \) is the over current limiter voltage (internal).
- \( R_s \) is the current detection resistance.
- \( V_{\text{dc}} \) is the input DC voltage.
- \( L_p \) is the primary inductance.
- \( t_{\text{delay}} \) is the delay time after detection of over current limiter.

![Figure 11. No AC Voltage Compensation Function](image1)

![Figure 12. Built-in AC Compensation Voltage](image2)

![Figure 13. Over Current Limiter Voltage](image3)
6. L. E. B. Blanking Period
When the MOSFET driver is turned ON, surge current flows through each capacitor component and drive current is generated. Therefore, when the SOURCE pin voltage rises temporarily, detection errors may occur in the over current limiter circuit. To prevent detection errors, DRAIN is switched from high to low and the SOURCE signal is masked for 250 ns by the on-chip LEB (Leading Edge Blanking) function.

7. SOURCE Pin Short Protection Function
When the SOURCE pin is shorted, excessive heat may destroy the IC. To prevent it from being damaged, these ICs have a built-in short protection function.

8. SOURCE Pin Open Protection
When the SOURCE pin becomes OPEN, excessive heat by noise may destroy the IC. To prevent it from being damaged, these ICs have a built-in OPEN protection circuit (auto recovery protection).

9. Output Over Load Protection Function (FB OLP Comparator)
The output overload protection function monitors the secondary output load status at the FB pin and stops switching whenever overload occurs. When there is an overload, the output voltage is reduced and current no longer flows to the photo coupler, so the FB pin voltage rises. When the FB pin voltage > \( V_{FOLP1A} \) continuously for the period \( t_{FOLP1} \), it is judged as an overload and switching stops. When the FB pin > \( V_{FOLP1A} \), the voltage goes lower than \( V_{FOLP1B} \) during the period \( t_{FOLP1} \), the overload protection timer is reset. The switching operation is performed during this period \( t_{FOLP1} \).

At startup, the FB voltage is pulled up to the IC's internal voltage, so operation starts at a voltage of \( V_{FOLP1A} \) or above. Therefore, at startup please set startup time within \( t_{FOLP1} \), so that the FB voltage becomes \( V_{FOLP1B} \) or less. Recovery is after the period \( t_{FOLP2} \), from the detection of FBOLP.

### Absolute Maximum Ratings (Ta=25 °C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Rating</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum Applied Voltage 1</td>
<td>( V_{MAX1} )</td>
<td>-0.3 to +32.0</td>
<td>V</td>
<td>VCC</td>
</tr>
<tr>
<td>Maximum Applied Voltage 2</td>
<td>( V_{MAX2} )</td>
<td>-0.3 to +6.5</td>
<td>V</td>
<td>SOURCE, FB, FADJ</td>
</tr>
<tr>
<td>Maximum Applied Voltage 3</td>
<td>( V_{MAX3} )</td>
<td>650</td>
<td>V</td>
<td>DRAIN</td>
</tr>
<tr>
<td>Drain Current Pulse</td>
<td>( I_{DP} )</td>
<td>12</td>
<td>A</td>
<td>( P_{W}=10 \mu s, ) Duty cycle=1 % (BM2P0161-Z)</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>( P_{d} )</td>
<td>1.00</td>
<td>W</td>
<td>(Note 1)</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>( T_{stg} )</td>
<td>-55 to +150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>( T_{jmax} )</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

**Caution 1:** Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

**Caution 2:** Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip. In case of exceeding this absolute maximum rating, design a PCB boards with power dissipation taken into consideration by increasing board size and copper area so as not to exceed the maximum junction temperature rating.

(Note 1) When mounted (on 74.2 mm x 74.2 mm, 1.6 mm thick, glass epoxy on single-layer substrate). Reduce to 8 mW/°C when Ta=25 °C or above.
Thermal Loss

The thermal design should set operation for the following conditions.

1. The ambient temperature $T_a$ must be 105 °C or less.
2. The IC’s loss must be within the power dissipation $P_d$.

The thermal abatement characteristics are as follows.

(PCI: 74.2 mm x 74.2mm x 1.6 mm, mounted on glass epoxy on single-layer substrate)

![Figure 14. DIP7K Thermal Abatement Characteristics](image)

### Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Rating</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Voltage Range 1</td>
<td>VCC</td>
<td>8.9 - 26.0</td>
<td>V</td>
<td>VCC pin voltage</td>
</tr>
<tr>
<td>Power Supply Voltage Range 2</td>
<td>$V_{DRAIN}$</td>
<td>- 650</td>
<td>V</td>
<td>DRAIN pin voltage</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>$T_{opr}$</td>
<td>-40 - +105</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

### Electrical Characteristics (unless otherwise noted, $T_a=25$ °C, $V_{CC}=15$ V)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Rating</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>[MOSFET Block]</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Between Drain and Source Voltage</td>
<td>$V_{BR(ODS)}$</td>
<td>650</td>
<td>V</td>
<td>$I_D=1$ mA / $V_{GS}=0$ V</td>
</tr>
<tr>
<td>Drain Leak Current</td>
<td>$I_{DSS}$</td>
<td>- 100</td>
<td>μA</td>
<td>$V_{DS}=650$ V / $V_{GS}=0$ V</td>
</tr>
<tr>
<td>On Resistance</td>
<td>$R_{DS(ON)}$</td>
<td>- 1.0 1.4</td>
<td>Ω</td>
<td>$I_D=0.25$ A / $V_{GS}=10$ V (BM2P0161-Z)</td>
</tr>
<tr>
<td>On Resistance</td>
<td>$R_{DS(ON)}$</td>
<td>- 3.0 3.6</td>
<td>Ω</td>
<td>$I_D=0.25$ A / $V_{GS}=10$ V (BM2P0361-Z)</td>
</tr>
</tbody>
</table>
### Electrical Characteristics – continued

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Specifications</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>[Circuit Current]</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Circuit Current (ON) 1</td>
<td>$I_{ON1}$</td>
<td>-</td>
<td>900</td>
<td>1450</td>
</tr>
<tr>
<td>Circuit Current (ON) 1</td>
<td>$I_{ON1}$</td>
<td>-</td>
<td>650</td>
<td>1050</td>
</tr>
<tr>
<td>Circuit Current (ON) 2</td>
<td>$I_{ON2}$</td>
<td>150</td>
<td>300</td>
<td>450</td>
</tr>
<tr>
<td><strong>[VCC Protection Function]</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VCC UVLO Voltage 1</td>
<td>$V_{UVLO1}$</td>
<td>12.50</td>
<td>13.50</td>
<td>14.50</td>
</tr>
<tr>
<td>VCC UVLO Voltage 2</td>
<td>$V_{UVLO2}$</td>
<td>7.50</td>
<td>8.20</td>
<td>8.90</td>
</tr>
<tr>
<td>VCC UVLO Hysteresis</td>
<td>$V_{UVLO3}$</td>
<td>-</td>
<td>5.30</td>
<td>-</td>
</tr>
<tr>
<td>VCC OVP Voltage 1</td>
<td>$V_{OVP1}$</td>
<td>26.0</td>
<td>27.5</td>
<td>29.0</td>
</tr>
<tr>
<td>VCC OVP Voltage 2</td>
<td>$V_{OVP2}$</td>
<td>22.0</td>
<td>23.5</td>
<td>25.0</td>
</tr>
<tr>
<td>VCC OVP Hysteresis</td>
<td>$V_{OVP3}$</td>
<td>-</td>
<td>4.0</td>
<td>-</td>
</tr>
<tr>
<td>VCC Recharge Start Voltage</td>
<td>$V_{CHG1}$</td>
<td>7.70</td>
<td>8.70</td>
<td>9.70</td>
</tr>
<tr>
<td>VCC Recharge Stop Voltage</td>
<td>$V_{CHG2}$</td>
<td>12.00</td>
<td>13.00</td>
<td>14.00</td>
</tr>
<tr>
<td>Latch Mask Time</td>
<td>$t_{LATCH}$</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>Thermal Shutdown Temperature 1</td>
<td>$T_{SD1}$</td>
<td>120</td>
<td>145</td>
<td>170</td>
</tr>
<tr>
<td>Thermal Shutdown Temperature 2</td>
<td>$T_{SD2}$</td>
<td>90</td>
<td>115</td>
<td>140</td>
</tr>
<tr>
<td><strong>[PWM Type DC/DC Driver Block]</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Oscillation Frequency 1</td>
<td>$f_{SW1}$</td>
<td>60</td>
<td>65</td>
<td>70</td>
</tr>
<tr>
<td>Oscillation Frequency 2</td>
<td>$f_{SW2}$</td>
<td>20</td>
<td>25</td>
<td>30</td>
</tr>
<tr>
<td>Frequency Hopping Width 1</td>
<td>$f_{DEL1}$</td>
<td>-</td>
<td>4.0</td>
<td>-</td>
</tr>
<tr>
<td>Hopping Fluctuation Frequency</td>
<td>$f_{CH}$</td>
<td>75</td>
<td>125</td>
<td>175</td>
</tr>
<tr>
<td>FADJ Source Current</td>
<td>$I_{BST}$</td>
<td>0.80</td>
<td>1.00</td>
<td>1.20</td>
</tr>
<tr>
<td>FADJ Comparator Voltage</td>
<td>$V_{BST}$</td>
<td>1.13</td>
<td>1.20</td>
<td>1.27</td>
</tr>
<tr>
<td>FADJ Max Burst Frequency</td>
<td>$f_{BST}$</td>
<td>-</td>
<td>0.833</td>
<td>-</td>
</tr>
<tr>
<td>Soft Start Time 1</td>
<td>$t_{SS1}$</td>
<td>0.30</td>
<td>0.50</td>
<td>0.70</td>
</tr>
<tr>
<td>Soft Start Time 2</td>
<td>$t_{SS2}$</td>
<td>0.60</td>
<td>1.00</td>
<td>1.40</td>
</tr>
<tr>
<td>Soft Start Time 3</td>
<td>$t_{SS3}$</td>
<td>1.20</td>
<td>2.00</td>
<td>2.80</td>
</tr>
<tr>
<td>Soft Start Time 4</td>
<td>$t_{SS4}$</td>
<td>4.80</td>
<td>8.00</td>
<td>11.20</td>
</tr>
<tr>
<td>Maximum Duty</td>
<td>$D_{MAX}$</td>
<td>68.0</td>
<td>75.0</td>
<td>82.0</td>
</tr>
<tr>
<td>Minimum ON Time</td>
<td>$t_{MIN}$</td>
<td>150</td>
<td>400</td>
<td>650</td>
</tr>
<tr>
<td>FB Pin Pull-Up Resistance</td>
<td>$R_{PB}$</td>
<td>23</td>
<td>30</td>
<td>37</td>
</tr>
<tr>
<td>ΔFB / ΔSOURCE Gain</td>
<td>Gain</td>
<td>3.00</td>
<td>4.00</td>
<td>7.00</td>
</tr>
<tr>
<td>FB Burst Voltage 1</td>
<td>$V_{BST1}$</td>
<td>0.220</td>
<td>0.280</td>
<td>0.340</td>
</tr>
<tr>
<td>FB Burst Voltage 2</td>
<td>$V_{BST2}$</td>
<td>0.260</td>
<td>0.320</td>
<td>0.380</td>
</tr>
<tr>
<td>FB Burst Hysteresis</td>
<td>$V_{BST3}$</td>
<td>-</td>
<td>0.040</td>
<td>-</td>
</tr>
<tr>
<td>FB Voltage of Starting Frequency Reduction</td>
<td>$V_{DLT}$</td>
<td>1.100</td>
<td>1.250</td>
<td>1.400</td>
</tr>
<tr>
<td>FB OLP Voltage 1a</td>
<td>$V_{FOLP1A}$</td>
<td>2.60</td>
<td>2.80</td>
<td>3.00</td>
</tr>
<tr>
<td>FB OLP Voltage 1b</td>
<td>$V_{FOLP1B}$</td>
<td>2.40</td>
<td>2.60</td>
<td>2.80</td>
</tr>
<tr>
<td>FB OLP ON Detect Timer</td>
<td>$t_{FOLP1}$</td>
<td>80</td>
<td>128</td>
<td>176</td>
</tr>
<tr>
<td>FB OLP OFF Timer</td>
<td>$t_{FOLP2}$</td>
<td>332</td>
<td>512</td>
<td>692</td>
</tr>
</tbody>
</table>
Electrical Characteristics – continued

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Specifications</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Over-Current Detection Voltage</td>
<td>$V_{\text{SOURCE}}$</td>
<td>0.375 0.400 0.425</td>
<td>V</td>
<td>$t_{\text{ON}}=0\ \mu\text{s}$</td>
</tr>
<tr>
<td>Over-Current Detection Voltage SS1</td>
<td>$V_{\text{SOURCE SS1}}$</td>
<td>0.050 0.100 0.150</td>
<td>V</td>
<td>0 ms to $t_{\text{SS1}}$ ms</td>
</tr>
<tr>
<td>Over-Current Detection Voltage SS2</td>
<td>$V_{\text{SOURCE SS2}}$</td>
<td>0.080 0.150 0.220</td>
<td>V</td>
<td>$t_{\text{SS1}}$ ms to $t_{\text{SS2}}$ ms</td>
</tr>
<tr>
<td>Over-Current Detection Voltage SS3</td>
<td>$V_{\text{SOURCE SS3}}$</td>
<td>0.130 0.200 0.270</td>
<td>V</td>
<td>$t_{\text{SS2}}$ ms to $t_{\text{SS3}}$ ms</td>
</tr>
<tr>
<td>Over-Current Detection Voltage SS4</td>
<td>$V_{\text{SOURCE SS4}}$</td>
<td>0.230 0.300 0.370</td>
<td>V</td>
<td>$t_{\text{SS3}}$ ms to $t_{\text{SS4}}$ ms</td>
</tr>
<tr>
<td>Leading Edge Blanking Time</td>
<td>$t_{\text{LEB}}$</td>
<td>120 250 380</td>
<td>ns</td>
<td></td>
</tr>
<tr>
<td>Over Current Detection AC Voltage Compensation Factor</td>
<td>$K_{\text{SOURCE}}$</td>
<td>12 20 28</td>
<td>mV/$\mu\text{s}$</td>
<td></td>
</tr>
<tr>
<td>SOURCE Pin Short Protection Voltage</td>
<td>$V_{\text{SOURCE SH}}$</td>
<td>0.020 0.050 0.080</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>SOURCE Pin Short Protection Time</td>
<td>$t_{\text{SOURCE SH}}$</td>
<td>1.80 3.00 4.20</td>
<td>$\mu\text{s}$</td>
<td></td>
</tr>
</tbody>
</table>

| [Circuit Current]                                |                         |                         |        |                             |
| Start Current 1                                  | $I_{\text{START1}}$    | 0.100 0.500 1.000       | mA     | VCC=0 V                     |
| Start Current 2                                  | $I_{\text{START2}}$    | 1.000 3.000 6.000       | mA     | VCC=10 V                    |
| OFF Current                                     | $I_{\text{START3}}$    | - 10 20                | $\mu\text{A}$ | Inflow current from the DRAIN pin after UVLO is released and when MOSFET is OFF |
| Start Current Switching Voltage                  | $V_{\text{SC}}$        | 0.800 1.500 2.100      | V      |                             |

(Note 2) Not 100% tested.

I/O Equivalence Circuit

<table>
<thead>
<tr>
<th>1 SOURCE</th>
<th>2 FADJ</th>
<th>3 GND</th>
<th>4 FB</th>
<th>5 VCC</th>
<th>6 DRAIN</th>
<th>7 DRAIN</th>
</tr>
</thead>
</table>

Figure 15. Equivalence Circuit Diagram
Operational Notes

1. **Reverse Connection of Power Supply**
   Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC’s power supply pins.

2. **Power Supply Lines**
   Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

3. **Ground Voltage**
   Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition.

4. **Ground Wiring Pattern**
   When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

5. **Recommended Operating Conditions**
   The function and operation of the IC are guaranteed within the range specified by the recommended operating conditions. The characteristic values are guaranteed only under the conditions of each item specified by the electrical characteristics.

6. **Inrush Current**
   When power is first supplied to the IC, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IC has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

7. **Operation Under Strong Electromagnetic Field**
   Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

8. **Testing on Application Boards**
   When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC’s power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

9. **Inter-pin Short and Mounting Errors**
   Ensure that the direction and position are correct when mounting the IC on the PCB. Incorrect mounting may result in damaging the IC. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

10. **Unused Input Pins**
    Input pins of an IC are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IC. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.
Operational Notes – continued

11. Regarding the Input Pin of the IC
This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.
When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

Figure 16. Example of monolithic IC structure

12. Ceramic Capacitor
When using a ceramic capacitor, determine a capacitance value considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

13. Area of Safe Operation (ASO)
Operate the IC such that the output voltage, output current, and the maximum junction temperature rating are all within the Area of Safe Operation (ASO).

14. Thermal Shutdown Circuit (TSD)
This IC has a built-in thermal shutdown circuit that prevents heat damage to the IC. Normal operation should always be within the IC’s maximum junction temperature rating. If however the rating is exceeded for a continued period, the junction temperature (Tj) will rise which will activate the TSD circuit that will turn OFF power output pins. When the Tj falls below the TSD threshold, the circuits are automatically restored to normal operation.
Note that the TSD circuit operates in a situation that exceeds the absolute maximum ratings and therefore, under no circumstances, should the TSD circuit be used in a set design or for any purpose other than protecting the IC from heat damage.

15. Over Current Protection Circuit (OCP)
This IC incorporates an integrated overcurrent protection circuit that is activated when the load is shorted. This protection circuit is effective in preventing damage due to sudden and unexpected incidents. However, the IC should not be used in applications characterized by continuous operation or transitioning of the protection circuit.
### Ordering Information

BM2P0x61 - Z

<table>
<thead>
<tr>
<th>MOSFET ON Resistor</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1: 1.0 Ω (Typ)</td>
<td></td>
</tr>
<tr>
<td>3: 3.0 Ω (Typ)</td>
<td></td>
</tr>
</tbody>
</table>

### Making Diagram

**DIP7K (TOP VIEW)**

- Part Number Marking
- LOT Number

### Part Number Marking

<table>
<thead>
<tr>
<th>Part Number Marking</th>
<th>Product Name</th>
<th>MOSFET ON Resistor</th>
</tr>
</thead>
<tbody>
<tr>
<td>BM2P0161</td>
<td>BM2P0161-Z</td>
<td>1.0 Ω (Typ)</td>
</tr>
<tr>
<td>BM2P0361</td>
<td>BM2P0361-Z</td>
<td>3.0 Ω (Typ)</td>
</tr>
</tbody>
</table>
Physical Dimension and Packing Information

<table>
<thead>
<tr>
<th>Package Name</th>
<th>DIP7K</th>
</tr>
</thead>
</table>

(UNIT: mm)

PKG:DIP7K
Drawing No. EX01-0076

< Container Information >

<table>
<thead>
<tr>
<th>Container</th>
<th>Tube</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quantity</td>
<td>2000pcs</td>
</tr>
<tr>
<td>Direction of feed</td>
<td>Packing orientation is same in tube</td>
</tr>
</tbody>
</table>

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TSZ22111 • 15 • 001

18/19

## Revision History

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>15.May.2018</td>
<td>001</td>
<td>New Release</td>
</tr>
<tr>
<td>20.Mar.2019</td>
<td>002</td>
<td>P1 Modify the size of package</td>
</tr>
</tbody>
</table>
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(Note1) Medical Equipment Classification of the Specific Applications

<table>
<thead>
<tr>
<th>JAPAN</th>
<th>USA</th>
<th>EU</th>
<th>CHINA</th>
</tr>
</thead>
<tbody>
<tr>
<td>CLASS Ⅲ</td>
<td>CLASS Ⅲ</td>
<td>CLASS Ⅱ b</td>
<td>CLASS Ⅲ</td>
</tr>
<tr>
<td>CLASS Ⅳ</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

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- [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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- [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
- [f] Sealing or coating our Products with resin or other coating materials
- [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
- [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

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   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

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3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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